

Amendments to the Specification

Please amend paragraph 1 on page 1 of the specification as follows

[0001] This application is related to a Paul L. Master et al., U.S. patent application Ser. No. 10/384,486, entitled "Adaptive Integrated Circuitry With Heterogeneous And Reconfigurable Matrices Of Diverse And Adaptive Computational Units Having Fixed, Application Specific Computational Elements", filed March 7, 2003, commonly assigned to QuickSilver Technology, Inc., and incorporated by reference herein, ~~with priority claimed for all commonly disclosed subject matter (the "related application")~~, which is a continuation-in-part of Paul L. Master et al., U.S. patent application Ser. No. 09/815,122, entitled "Adaptive Integrated Circuitry With Heterogeneous And Reconfigurable Matrices Of Diverse And Adaptive Computational Units Having Fixed, Application Specific Computational Elements", filed Mar. 22, 2001, now U.S. Patent No. 6,836,839, commonly assigned to QuickSilver Technology, Inc.